

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Ken Gary Pomaranski, et al.

Confirmation No.:

Application No.: 10/623,099

Examiner: unknown

Filing Date: July 18, 2003

Group Art Unit:

Title: TARGETED FAULT TOLERANCE BY SPECIAL CPU INSTRUCTIONS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ (X) under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ () under 37 CFR 1.97 (c) together with either a:
☐ () Statement under 37 CFR 1.97(e), or
☐ () a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ () under 37 CFR 1.97 (d) together with a:
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☐ () A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

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Typed Name: James K. Okamoto

Signature: James K. Okamoto

Respectfully submitted,

Ken Gary Pomaranski, et al.

By James K. Okamoto

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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/623,099
Filing Date	July 18, 2003
First Named Inventor	Ken Gary Pomaranski
Art Unit	
Examiner Name	unknown
Attorney Docket Number	200310409-1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1E	JOYDEEP RAY, ET AL. " Dual Use of Superscalar Datapath for Transient-Fault Detection and Recovery", International Symposium on Microarchitecture archive, 2001, pages 214-224, IEEE Computer Society, Washington, DC, USA.	

Examiner Signature		Date Considered	
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